# NCE N-Channel Enhancement Mode Power MOSFET

#### **DESCRIPTION**

The NCE55H12 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **GENERAL FEATURES**

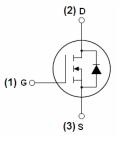
- $V_{DS}$  =55V, $I_{D}$  =120A  $R_{DS(ON)}$  < 5.5mΩ @  $V_{GS}$ =10V (Typ:4.1mΩ)
- High density cell design for ultra low Rdson
- Fully characterized Avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

#### **Application**

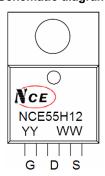
- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply

100% UIS TESTED!

100% ΔVds TESTED!



#### Schematic diagram



Marking and pin Assignment



TO-220 top view

#### **Package Marking And Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE55H12	NCE55H12	TO-220	-	-	-

### Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	55	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	120	Α
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	85	Α
Pulsed Drain Current	I <sub>DM</sub>	420	Α
Maximum Power Dissipation	P <sub>D</sub>	200	W
Derating factor		1.33	W/℃
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	1100	mJ
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 175	$^{\circ}$



# NCE55H12

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case(Note 2)	R <sub>eJC</sub>	0.75	°C/W	
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# Electrical Characteristics (TA=25°Cunless otherwise noted)

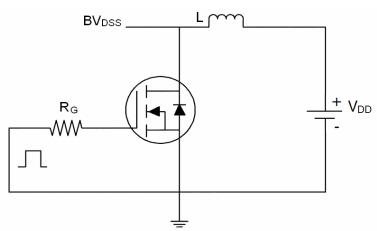
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics					•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA		65	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =55V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A	-	4.1	5.5	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =25V,I <sub>D</sub> =40A	50	-	-	S
Dynamic Characteristics (Note4)	<u>.</u>				•	•
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =25V,V <sub>GS</sub> =0V, F=1.0MHz		4900	-	PF
Output Capacitance	C <sub>oss</sub>			470	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>			460	-	PF
Switching Characteristics (Note 4)	•		•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	20	-	nS
Turn-on Rise Time	t <sub>r</sub>	1,, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	-	19	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{DD}$ =30V, $I_{D}$ =2A $V_{GS}$ =10V, $R_{GEN}$ =2.5 $\Omega$		70	-	nS
Turn-Off Fall Time	t <sub>f</sub>			30	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =30V,I <sub>D</sub> =30A,		125	-	nC
Gate-Source Charge	Q <sub>gs</sub>			24	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	49	-	nC
Drain-Source Diode Characteristics	-		U.		I.	I.
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =40A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	120	Α
Reverse Recovery Time	t <sub>rr</sub>			37	-	nS
Reverse Recovery Charge	Qrr	Tj=25°C, $I_F$ =75A,di/dt=100A/ $\mu$ s (Note3)	-	58	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by		ed by LS	LS+LD)	
		1				

### Notes:

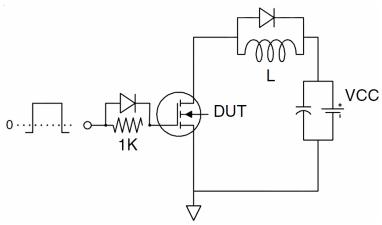
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25  $^{\circ}\text{C}$  ,VDD=28V,VG=10V,L=0.5mH,Rg=25 $\Omega$

# **Test circuit**

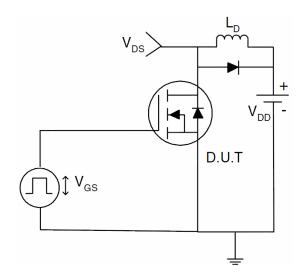
# 1) E<sub>AS</sub> test Circuits



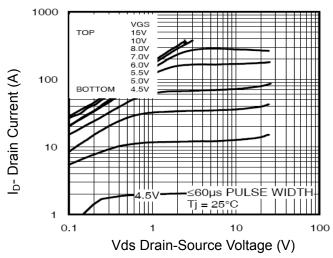
# 2) Gate charge test Circuit:



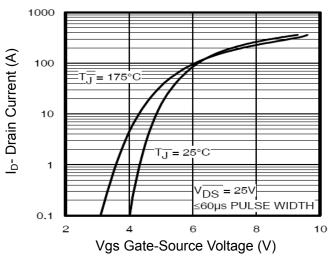
# 3) Switch Time Test Circuit:



# TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

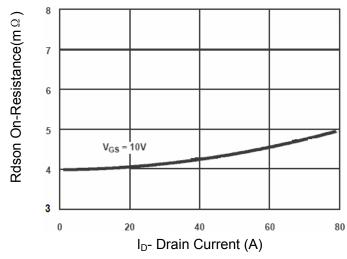


Figure 3 Rdson- Drain Current

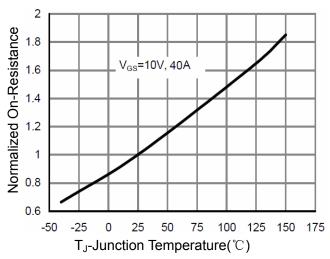


Figure 4 Rdson-JunctionTemperature

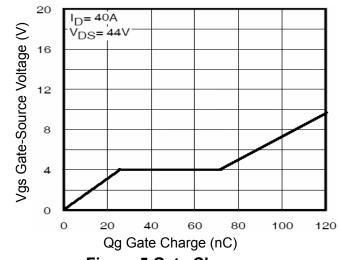


Figure 5 Gate Charge

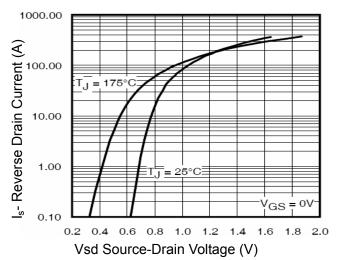


Figure 6 Source- Drain Diode Forward

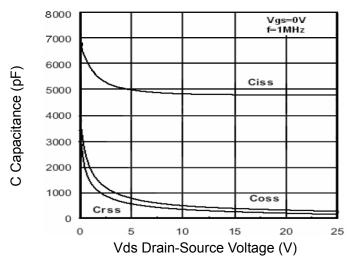


Figure 7 Capacitance vs Vds

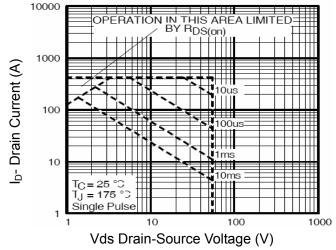


Figure 8 Safe Operation Area

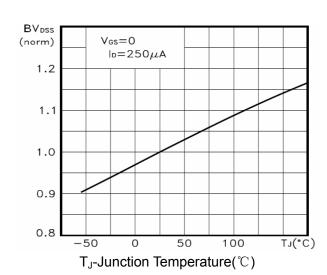


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

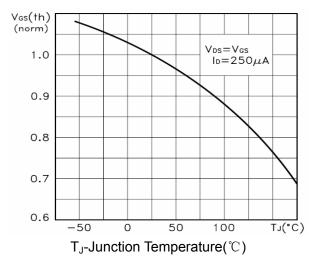


Figure 10 V<sub>GS(th)</sub> vs Junction Temperatur

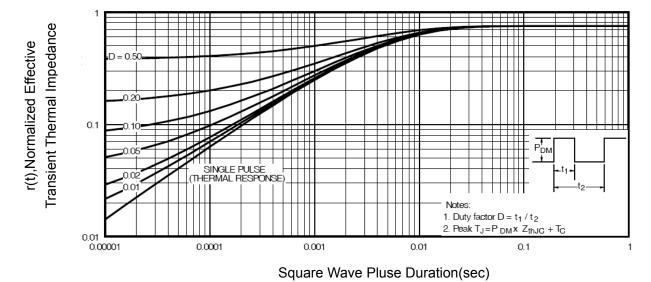
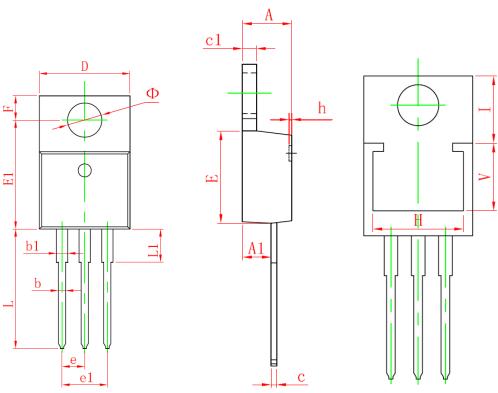


Figure 11 Normalized Maximum Transient Thermal Impedance

# **TO-220-3L Package Information**



Crymh ol	Dimensions	In Millimeters	Dimensions In Inches			
Symbol	Min	Max	Min	Max		
A	4.470	4.670	0.176	0.184		
A1	2.520	2.820	0.099	0.111		
b	0.710	0.910	0.028	0.036		
b1	1.170	1.370	0.046	0.054		
c	0.330	0.650	0.013	0.026		
c1	1.200	1.400	0.047	0.055		
D	10.010	10.350	0.394	0.407		
E	8.500	8.900	0.335	0.350		
E1	12.060	12.460	0.475	0.491		
e	2.540 (TYP.)		0.100 (TYP.)			
e1	4.980	5.180	0.196	0.204		
F	2.590	2.890	0.102	0.114		
Н	8.44	8.440 REF.		332 REF.		
h	0.000	0.300	0.000	0.012		
L	13.400	13.800	0.528	0.543		
L1	3.560	3.960	0.140	0.156		
V	6.360 REF.		0.250 REF.			
I	6.300 REF.		0.248 REF.			
Ф	3.735	3.935	0.147	0.155		

NCE55H12

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